

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	3097	jitter with pll	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:07
L3	320	jitter with pll with vco	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:07
L4	9	jitter with pll with vco with measurement	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:07
L5	1	"10/066019"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L6	8	"6236524".pn. "6137832".pn. "6005892".pn. "4727578".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L7	16	switch adj matrix with equaliz\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L8	0	input adj ports and output adj ports and eqializ\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L9	3361	input adj ports and output adj ports and equaliz\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42

L10	164	input adj ports with output adj ports with equaliz\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L11	14	input adj ports with output adj ports with equaliz\$5 with loss	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L12	0	input adj ports with output adj ports with equaliz\$5 with loss and switch with matrix	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L13	0	input adj ports with output adj ports with equaliz\$5 with loss and switch with matrix	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L14	5	input adj ports same output adj ports same equaliz\$5 same loss and switch with matrix	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L15	53	switch with matrix and equaliz\$5 with loss	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L16	61375	hasegawa.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L17	2	"5191431".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42

L18	344	asymmetrical with compression	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L19	48	asymmetrical adj compression	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L20	217	access with request with memory with throughput	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L21	72	access adj request with memory with throughput	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L22	45	access adj request near memory with throughput	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L23	4	access adj request near memory near throughput	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L24	3	"10091503"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L25	1	"10/091503"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42

L26	25	switch near matrix with equaliz\$6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L27	0	switch\$3 near matrix with equaliz\$6 with jitter with loss	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L28	4	switch\$3 near matrix with equaliz\$6 and jitter and loss	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L29	12	switch near matrix with equaliz\$6 and induc\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L30	0	equaliz\$6 with inductance with resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L31	0	equaliz\$6 with inductor with resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L32	0	equaliz\$6 same inductor same resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L33	1	equaliz\$6 and inductor and resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42

L34	2	equaliz\$6 and induct\$4 and ressistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L35	3	switch\$4 and induct\$4 and ressistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L36	79804	switch\$4 and induct\$4 and resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L37	132	equaliz\$6 with inductor with resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L38	58	equaliz\$6 with inductor with resistor and switch\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L39	11	equaliz\$6 with inductor with resistor with switch\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L40	1	equaliz\$6 with inductor with resistor and switch\$4 and skin	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L41	1512	375/229	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42

L42	583	switch near matrix and equaliz\$6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L43	11	L41 and L42	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L44	1880	375/232	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42
L45	4	L42 and L44	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 09:42